IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,450,659 Atty Docket: 10021250-01-US

Issued: November 11, 2008

Serial No.: 10/814.472

Inventor(s): Corredoura et al.

Title: DIGITAL MODULATOR EMPLOYING A POLYPHASE UP-CONVERTER

STRUCTURE

REQUEST FOR ISSUANCE OF CERTIFICATE OF CORRECTION

Attn: Certificate of Correction Branch Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450

Sir

Agilent Technologies, Inc., assignee of the entire interest in the above-identified patent, respectfully requests that a Certificate of Correction be issued in accordance with 37 CFR §§ 1.322 and/or 1.323.

Enclosed herewith is a completed copy of Form PTO/SB/44 describing the various errors involved in the above-identified patent.

- 1. A The errors are a result of USPTO mistakes. NO FEE is required.
- 2.

 The errors are a result of Applicant's mistakes. \$100 Fee required.
- 3. The errors are a result of both USPTO and Applicant's mistakes. \$100 Fee required.

We respectfully request that a Certificate of Correction be issued to correct the errors, which occurred in good faith. Please return to us an executed copy of the Certificate of Correction for attachment to our Letters Patent.

Respectfully submitted,

/Judy Liao Shie/ Judy Liao Shie Reg. No. 50,305

Agilent Technologies, Inc. IP Operations P.O. Box 7599 Loveland, CO 80537

Approved for use through 08/31/2010. OMB 0651-0033
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(Also Form PTO-1059)

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PATENT NO	: 7,450,659		Page 1 of 1
APPLICATION NO.: 10/814,472			
ISSUE DATE	: November 11, 2008		
INVENTOR(S)	: Corredoura et al.		
It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:			
On the face page, in field (75), in "Inventors", in column 1, line 2, delete "Sunnyvale," and insert Fremont,, therefor.			
In column 6, line 60, in Claim 6, delete "filter" and insert filters, therefor.			
In column 6, line 62, in Claim 6, delete "component." and insert components, therefor.			